

Amendments to the Specification:

Please replace the paragraph beginning at page 9, line 7 with the following amended paragraph:

Another instance where speculative registers may be advantageous is for use with watch points. Watch points may be implemented to aid programmers in the debugging process of a programmable processor. Watch points may be set up to monitor or "watch" particular instructions, particular types of instructions, accesses to a specified memory location, or any other condition associated with an instruction. If, for instance, an instruction being watched is executed in the pipeline a defined number of times, the system may implement a particular exception.